IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Application Serial No | 10/053,731 |
|--|---------------------------------------|
| Filing Date | January 18, 2002 |
| Inventor | Jonathan Christopher Hardwick et al. |
| | 2123 |
| Examiner | Sharon, Ayal |
| Attorney's Docket No | MS183174.1/40062.164US01 (MS1-1423US) |
| Confirmation No | 4229 |
| Title: Evaluating Hardware Models Having Resource Contention | |

RESPONSE TO OFFICE ACTION DATED AUGUST 18, 2005

To:

Commissioner for Patents

PO Box 1450

Alexandria, Virginia 22313-1450

From:

Allan T. Sponseller (Tel. 509-324-9256 x215; Fax 509-323-8979)

Customer No. 22801

Sir:

In response to the Office Action of August 18, 2005, in connection with the above-identified application, the following remarks are submitted. Favorable consideration is respectfully requested.

A detailed listing of the claims is provided below. A status identifier is provided for each claim in a parenthetical expression following each claim number.